

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Shinya Ito	Examiner:	Unassigned
Serial No.:	Unassigned	Art Unit:	Unassigned
Filed:	Herewith	Docket:	17381
For:	SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME	Dated:	January 23, 2004

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Patent Application Publication No. 2001-85683, dated March 30, 2001;
2. Japanese Patent Application Publication No. 3-14241, dated January 22, 1991;
3. Japanese Patent Application Publication No. 3-79033, dated April 4, 1991;
and


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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 23, 2004.

Dated: January 23, 2004



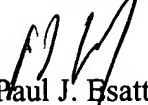
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4. Japanese Patent Application Publication No. 11-340456, dated December 10, 1999.

Applicant is submitting copies of the above-cited references. The relevance of the above-identified references has been described in the specification.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,


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